We are looking for you to join our international team of successful Chip Design and Verification Engineers in our *Thessaloniki Design Center*

CHIP® GLOBE
lighting the chip world

Junior RTL Design Engineer (f/m/d)

ChipGlobe is focusing on RTL Design – and Verification Consulting with both Insourcing (at Customer premises) and Outsourcing (ChipGlobe Design Center) working models. Our teams work in a global setup with ASIC design, verification and software teams in major semiconductor companies, focusing on markets in automotive, telecommunications, security and networking. ChipGlobe headquarters are located in Munich, Germany with a ChipGlobe Entity in Singapore and a ChipGlobe Design Centers in Dresden - Germany, Belgrade – Serbia, Ho Chi Minh City – Vietnam and Thessaloniki - Greece. We are more than 90 engineers with an experienced management team. Based on the huge experience of our staff we manage and execute projects in a well-communicated, success-proven and sustainable way. We love what we do, and we do what we love.

ChipGlobe Offering:

- ✓ Opportunity to work for stable, expanding German company with mature management, that is technically involved
- ✓ Excellent working environment, encouraging both technical and personal involvement
- ✓ Work together with a team of 90+ engineers across multiple expertise domains in a global team setup
- ✓ Strategic partnerships with leading semiconductor companies
- ✓ Opportunity to travel abroad and work on client's site
- ✓ New and challenging innovative projects

Job requirements – Technical Skills

- ✓ Good knowledge in digital electronics and RTL coding (Verilog, VHDL or SystemVerilog)
- ✓ 0-2 years of experience in digital design
- ✓ Experience with IP creation and maintenance
- ✓ Able to run linting tools (e.g. Spyglass), analyze results and fix the code
- ✓ Able to run trial Synthesis, understand basics of STA, improve code for physical implementation
- ✓ Able to understand specifications and requirements (requirement driven flow)
- ✓ Able to break down abstract target spec. into design, document design work
- ✓ Idea of safety and security
- ✓ Data management (e.g. ClearCase)

Job requirements – Soft Skills

- ✓ **Communication**
  - Able to abstract technical details
  - Good communication skills, enjoys working in an international team with some communication to Munich headquarters and EDA vendors
  - Good level of spoken and written English – must be able to understand and create technical documentation - presentation and listening skills are required
- ✓ **Team player**
  - Committed to the team and tasks
  - Helping team members, sharing knowledge
- ✓ **Self-driven and highly motivated**
  - Self-motivated, embracing the task
  - Highlight potential issues, regular reporting
  - Clean and organized working style
- ✓ **Solution oriented attitude, pragmatic. Good problem-solving skills**
  - Following and improving processes
- ✓ **Organized, trusted, committed to a long-term employment at Chipglobe**

If you are interested in this position, please send your CV and the reason why you would like to join Chipglobe to:

employment@chipglobe.com