

We are looking for you to join our international team of successful Chip Design and Verification Engineers in our *ChipGlobe d.o.o. Belgrade Design Center* for 3 months



Internship – RTL Design for ASICs (f/m/d)

ChipGlobe is focusing on RTL Design– and Verification Consulting with both Insourcing (at Customer premises) and Outsourcing (ChipGlobe Design Center) working models. Our teams work in a global setup with ASIC design, verification and software teams in major semiconductor companies, focusing on markets in automotive, telecommunications, security and networking. ChipGlobe headquarters are located in Munich, Germany with a ChipGlobe Entity in Singapore and a ChipGlobe Design Centers in Belgrade, Serbia and Ho Chi Minh City in Vietnam. We are more than 90 experts with an experienced management team. Based on the huge experience of our staff we manage and execute projects in a well-communicated, success-proven and sustainable way. We love what we do, and we do what we love.

We are offering you a practical and latest Industry Trend Knowledge build-up

- ✓ Paid Internship is offered to fresh graduates or final year students of Electronics/Computer Engineering in a stable, German Chip Design/Verification Company for Semiconductors, aligned in its business ethics and conduct with major tier 1 companies.
- ✓ Opportunity to work with access to a professional Software and EDA environment of big semiconductor companies.
- ✓ Work on a commercial project, i.e. conversion of IP in VHDL to SystemVerilog based on practical training materials developed from numerous projects. This will give you a kick-start in your expertise.
- ✓ Stable and organized German working environment.
- ✓ Intern will have adequate support from senior engineers of ChipGlobe, which will guide Intern during the internship. Good results and development of new skills ensured.
- ✓ Internship will last 3 months, and the job could be offered to a successful candidate.
- ✓ Internship will be held in modern business complex GREEN HEART in Novi Beograd.
- ✓ Come and work for us and have a great kick-start of a Successful Career in Semiconductors!

Job requirements – Technical Skills

- ✓ Basic knowledge in digital electronics and RTL coding (Verilog, VHDL or SystemVerilog).
- ✓ General programming skills.

Job requirements – Soft Skills

- ✓ **Communication**
 - Able to abstract technical details
 - Good communication skills, enjoys working in an international team with some communication to Munich headquarters and EDA vendors
 - Good level of spoken and written English – must be able to understand and create technical documentation
- ✓ **Team player**
 - Committed to the team and task
 - Helping team members, sharing knowledge
- ✓ **Self-driven and highly motivated**
 - Self-motivated, Embracing the task
 - Highlight potential issues, Organized working style
- ✓ **Solution oriented attitude, pragmatic. Good problem solving skills**
 - Following and improving processes

If you are interested in this internship, please send your CV and the reason why you would like to apply for this internship to:

internship@chipglobe.com – **Deadline for submission is 15 February 2022**